

CURRICULUM VITAE

P. PRABHU THAPASWINI, M.Tech.
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Educational Qualifications:

Qualification/ Specialization	Board/University	Year passed	Class
M.Tech (VLSI design)	VIT University	2016	Distinction
B.E (ECE)	Osmania University (Methodist College of Engineering & Technology)	2014	Distinction
Intermediate (MPC)	Board of Intermediate Education	2010	Distinction
S.S.C	Board of Secondary Education	2008	Distinction

Teaching and Research Experience:

1. Academic Consultant, SPMVV, Tirupati (01-08-2022 - till date)
2. Academic Consultant, S.V. University, Tirupati (2019 - 2020)
3. Teaching assistant and Research scholar in National University of Singapore (Aug,2017-Dec,2018).

No. of Publications: 2

Scopus - 1

International - 1

Technical Skills:

H/W Programming Languages : VHDL and Verilog
Scripting languages : Perl and Tcl
Tools/Softwares : Matlab, Cadence, Tanner Tools, Gwiddyon, Sentaurus–TCAD and Xilinx
Operating Systems : Linux and Windows

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