

## Profile

**T SRINIVASA PADMAJA**

ASSISTANT PROFESSOR

ECE

School of Engineering and Technology

Sri Padmavati Engineering College

Tirupati – 517502

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### Academic Qualifications

Class	Board/University	Year of Passing	Specialization/Area
Ph.D	JNTUA	Pursuing	Digital Image Processing
M.Tech	SRM University	2013	VLSI Design
B.Tech	JNTUA	2011	ECE
Intermediate	State board	2007	Mathematics, Physics and Chemistry
SSC	State board	2005	

**Ph. D Thesis Title: -**

**Total Experience: 7**

Organization	Position	From	To	No. of Years
S.P.M.V.V, tirupati	Assistant Professor	28-06-2017	Till date	3
Siddhartha institute of engineering & technology, Puttur	Assistant Professor	02-07-2014	29-04-2017	3
Modugula kalavathamma institute of engineering & technology, Rajampet	Assistant Professor	03-06-2013	30-06-2014	1

### Research Guidance

S.No	Name of the Candidate	Degree Awarded M.Phil/Ph.D	Year of Award	Title of the Thesis
-	-	-	-	-

**List of Subjects Taught:**

1. VLSI Design
2. Low power VLSI
3. VLSI system design
4. Microprocessors & microcontrollers
5. Pulse and digital circuits
6. Antenna wave & propagation
7. Electronic devices & circuits
8. Digital integrated circuit analysis
9. Cellular and mobile communications
10. Wireless communication networks

**Areas of Research Interest:**

- VLSI Design
- Digital electronics

**Research projects completed:**

S. No.	Title of the project	Date of sanction and duration	Grant received	Funding agency	PI or Co-PI
-	-	-	-	-	-

**Projects applied/sanctioned/ongoing:**

S. No.	Project Title	Applied to (Funding Agency)	Applied on Month and Year	Estimated Cost (Rs)	Status
-	-	-	-	-	-

**Consultancy:**

S.No.	Project Title	Client	Revenue Generated (Rs.)	Month, Year	Status
-	-	-	-	-	-

**Number of Ph.D scholars Registered : -**

**Number of PG Projects Guided : 4**

**Number of UG Projects Guided : 12**

**Books published**

S.No.	Name of the Authors	Title of the Book	Publisher Name	ISBN/ISSN	National/International	Year of Publication
-	-	-	-	-	-	-

**Book Chapters Published:**

S.No.	Name of the Authors	Title of the Chapter	Title of the Book	Publisher Name	ISBN/ISSN	National/International	Year of Publication
-	-	-	-	-	-	-	-

**Faculty Research Publications:**

Year	Papers Published in International Journals	Papers Published in National Journals	Total	International Conference Publications	National Conference Publications	Total
2018	2	-	2	-	-	-
2015	1	-	1	-	-	-
2013	-	-	-	1	-	1

**Membership in Professional organizations: -****List of Publications in International Journals:**

1. T. Srinivasa Padmaja, C. M. Sri Priya (2018, July). Design and Implementation of Carry Look Ahead Adder In Quantum Dot Cellular Automata. *International Journal of Scientific Research in Computer Science, Engineering and Information Technology (IJSRCSEIT)*, Volume 3, Issue 5, ISSN : 2456-3307.
2. T.Srinivasa Padmaja ,S.Anuradha, (2018, Jun). Implementation of Layer 3 Vpns over Layer 2 Vpn Topologies Using Gns3 Software . *International Journal of research(IJR)*, Volume 05 Issue 16, ISSN: 2348-6848.
3. T Srinivasa Padmaja, R Siva Kumar , (2015, Aug). Design of a Low-Power Efficient Double Edge Trigger Flip Flop. *International Journal Of Pure And Applied Research In Engineering And Technology(IJPRET)*, Volume 3, ISSN: 2319-507X.

**List of Publications in National Journals: -**

**List of Publications in International Conferences:**

1. T.SRINIVASA PADMAJA, K. FERENTS KONI JIAVANA (april,2013). Design of Prescalar For Optimizing Power And Voltage And Its Applications. *International conference on computational intelligence & Advanced Manufacturing Research(ICCIAMR)*, ISBN: 978-81-924031-7-511.

**List of Publications in National Conferences:**

1. T srinivasa padmaja, Siddhartha institute of engineering & technology (jun, 2015). Design of a low power efficient double edge trigger flip-flop.

**As a resource person in various events (Conferences – session chair/Seminars/Workshops/FDPs/Training Programs/Guest Lectures...)**

S.No.	Name of the Event	Organized by	Date	Number of Days
-	-	-	-	-

**List of Events (Conferences/Seminars/Workshops/FDPs/Training Programs/...) organized:**

S.No.	Name of the Event	Organized by	Date	Number of Days
1	National conference on VLSI and Embedded systems	SRM university	14 <sup>th</sup> -15 <sup>th</sup> feb, 2013	2

**List of Events (Conferences/Seminars/Workshops/FDPs/Training Programs/...) attended:**

S.No.	Name of the Event	Organized by	Date	Number of Days
1	Faculty development program	Texas Instrument program, AITS, Kadapa	22 <sup>nd</sup> & 23 <sup>rd</sup> dec,2014	2
2	Indian national science congress	SV university	3 <sup>rd</sup> -7 <sup>th</sup> jan,2017	5
3	National workshop on e-learning, MOOCS and basic ICT tools for Educators	MHRD, SGBT college, university of Delhi	26 <sup>th</sup> -27 <sup>th</sup> march, 2018	2
4	Big data analytics & IoT workshop	SSIIE-TBI,SPMVV, Tirupati	27,28&29 April, 2018	3

5	Big data analytics workshop	SOET,SPMVV,Tirupati	1 <sup>st</sup> &2 <sup>nd</sup> dec,2018	2
6	NACC Assesment Webinar	SOET,SPMVV,Tirupati	Aug 26,2020	1
7	Emerging trends in Electrical Engineering	KAVIKALGURU Institute, Nagpur	2 <sup>nd</sup> -4 <sup>th</sup> july,2020	2
8	Recent Advances in Electrical Engineering	Vemu institute of technology	3-7 june,2020	5
9	Effective role of teachers in Technical role of Education	Geethanjali Institute of technology	6 june, 2020	1

#### Administrative Experience

S.No.	Organization	Academic Year	Name of Administrative Work
1	School of Engineering and Technology, SPMVV	2017-Till Date	Library committee

#### Other Contributions

- Ratified as assistant professor on 4<sup>th</sup> sep 2014 at JNTUA, Anantapuramu.