



SRI PADMAVATI MAHILA VISVAVIDYALAYAM:TIRUPATI
(Women's University)

(FORM OF APPLICATION TO BE SUBMITTED BY THE CANDIDATES) –FOR THE ACADEMIC YEAR 2020-2021

1. **Name of the post applied for (Subject And Specialization)** a) Post : Assistant Professor[Junior]

b) Subject :Electronics and communication Engineering

c) Specialization :VLSI

2. **Name of the candidate in full** :C.M.Sri Priya

With address for communication and : D.NO. 20-49-S8-1318,Maruthi Nagar,
Tirupathi – 517 501

Mobile/Land line No :9177788993

3. **Date of Birth and Age (in completed years)** :27-10-1981,38

4. **Specify Community with sub category** :OC

5. **Qualifications: (Start with highest Degree)**

Examination Passed	Class	% of Marks	Year	University/Institution
(i)Ph.D. (VLSI, Pursuing)	I	95%	2019	SRM, University ,Chennai
(ii) M.Phil ()	NA	NA	NA	NA
(iii) M.Tech/ P.G (VLSI)	I	84%	2008	Jawaharlal Nehru Technological University Hyderabad.(JNTUH)
(iv) M.Ed.	NA	NA	NA	-
(v) B.Ed.	NA	NA	NA	NA
(vi) B.Tech/B.A/B.Sc./B.Com.	I	75.4%	2003	Jawaharlal Nehru Technological University Hyderabad.(JNTUH)

Ph.D. Topic : Implementation of Twisted Ring counter Using Test Vectors of BIST Architecture

5. (a) **Details of NET/GATE/CSIR** : -

(b) **Details of SET/SLET** : -

6. Research Publications:

(a)Publications

Publications	Published with ISSN/ISBN (Specify number)	
	National	International
1. Research Papers in Refereed Journals	-	-
2. Articles in Seminar Proceedings	-	-
3. Books	-	-

* Indicate only papers published in SCOPUS and CARE journals

b)Conferences/ Seminars/ Workshops etc.

	Attended	Presented	Organized
National Workshops	15	00	00
National Conference/Seminars	02	02	00
International Conference/ Workshops/ Seminars	00	00	00

7 (a). Research Guidance (Specify)

Number of PG Projects Guided :08

Number of UG Projects Guided : 30

(b). Research experience :

Working on “ **Journval in Testing of VLSI design**” under the guidance of Dr.V.Sarada at SRM University, Chennai from June 2019.

8.(a). Teaching Experience (At University level or UG level):

Name of the Post	Name of the Institution where worked	Period with dates		Total teaching Experience		
		U.G	P.G	DD	MM	YY
Assistant Professor(Junior)	School of Engineering and Technology, SPMVV, Tirupati.	21 st June'2017 to Till date	21 st June'2017 to Till date	00	00	03
Assistant Professor	Sri VidyanikethanEngg College, Tirupathi	1 st June'2008To 30 th April 2010	1 st June'2008To 30 th April 2010	00	00	02

Assistant Professor	Sri Srinivasa College of Engg and Tech,Chittoor	1 st June '2004 to 31 st April 2008	1 st June '2004 to 31 st April 2008	00	00	05
Total length of teaching experience up to 01-05-2020				00	00	10

(b). Administrative Experience:

Name of the Post	Name of the Institution where worked	Period with dates	Total Admin. Experience		
			DD	MM	YY
Entrepreneur committee Member	School of Engineering and Technology, SPMVV	June 2017 to April 2020	00	10	02
Admission committee Member	School of Engineering and Technology, SPMVV	June 2017 to April 2020	00	10	02
APSSDC committee Member	School of Engineering and Technology, SPMVV	June 2017 to April 2020	00	10	02

9. Contribution to the Academics/Research in last two years (2018-2019, 2019-2020)

List of Publications in International Journals:

1. **C.M.Sripriya, T.SrinivasaPadmaja.** (2018, july). Design and Implementation of Carry Look Ahead Adder In Quantum Dot Cellular Automata. In International Research In computer Science Engineering and Information Technology (IJSRCSEIT| 2456-3307,3(3), 2018. **ISSN: 2456-3307** (IF: 5.2)
2. C.M.Sripriya, Vasavi (2018, july). Energy Efficient D2D communication based Retransmission Schemes for Reliable Multicast in wireless cellular Networks Analysis of IEEE 802.15.4 sensor IEEE (volume 6,no.4,may 2015)
3. C.M.Sripriya,T.Silpa (2017, july). Transient Analysis of IEEE 802.15.4 sensor Marco Gribaudo Daniele AlessanroNordiomemr ,IEEE an carla –fabianachiasseiri (volume 10,no.4,April 2011
4. C.M.Sripriya, Prathyusha (2017, july). PAPR reduction in FBMC with Half complexity trellis based SLM In 2016IEEE International Conference on Signal Processing, Communication, Power and Embedded System (SCOPES)(pp. 2052-2063)

5. C.M.Sripriya, Sai pallavi (2016, july). PAPR reduction in OFDM using eue complexity PTS with companding #rdIntenationalconence in Avances in Electrical ,Electronics Information communication Bio_informatics (AEEICB17)

10. Any other relevant particulars for purpose of Screening Committee of performance appraisal
 1. Outcome based education is Introduced
 2. Question papers are prepared according to Blooms Taxonomy
 3. Assignment Booklets and Question are prepared
 4. Various association activities are conducted every week
 5. Conducted online classes and project viva –vice using ,online labs Using MHRD website during Lockdown
 6. Worked as a class Teacher for ECE II year and III year students
 7. Prepared manuals for VHDL labs
 8. Prepared the Syllabus for
 1. Electronic circuits Lab
 2. Electronic circuits Lab
 3. IC Applications Lab
 4. Electronic Devices and circuits Lab
 5. Basic Electronics Lab
 6. Syllabus for the subject Electronic Circuits
 7. Electronic Devices and circuits
 9. Taken extra classes for IV year students for preparing students for placement
 10. Conducted seminars for Mtech final year student and guided her in the project.
 7. Participated and conducted competitions in Virinchi-2017-2020 as
 - i. Digitrix
 - ii. Paper presentations
 - iii. Posters
 8. Worked for AICTE to prepare the subject Question papers in Blooms Taxonomy

Taxonomy, Prepared COs and POs

S.No.	Name of the Event	Organized by	Date	Number of Days
1.	Academic Writing	IIT ,Tirupathi	7 th to 9 th Feb 2020	3
2	FDP on VLSI Digital circuits and Testing Techniques	Miracle Educational society and Institutions, HYD	21 st to 23 rd May 2020	3
3.	Webinar on Testing of VLSI circuits	SRM University	25 th to 27 th May 2020	3
4	Building Rural Resilience	SPMVV	4 th to 8 th Dec 2019	5
5.	Big Data Analysis	SPMVV	1 st and 2 nd Dec 2019	2
6	Online NPTEL course in Signals and Systems	IIT Bombay	April 2019	4months
7	FDP-1 on Pedagogy for online and Blender Teaching Learning Process	IIT Bombay	30 th Oct 2018 to Dec 13 th 2018	2 months
8.	Workshop on IUCEE-International Engineering Educator certification	APSSDC	3 rd Aug 2017 to 5 th AUG 2017	2
9.	Workshop on Instructional Design and Delivery Systems	Sri Vidhyanikethan Engg and Technology	28 th to 30 th June 2010	2
10	Workshop on Embedded Systems and VLSI	Sri Vidhyanikethan Engg and Tech	19 th June to 1 st July 2006	2 weeks
11	Workshop on VLSI Design and Nano Electronics	SVCE	March 30 to 31 st 2007	2 Days
12	Workshop on Emerging Trends in VLSI design	SITAMS	15 th to 18 th Oct 2007	3 Days
13	Conference on The Institute of Electronics and Telecommunications Engg	Karpagam college	22 nd to 24 th September 2007	2 Days

Honors & Awards

- Received **L V PASAD MEMORIAL** Gold Medal in M.Tech. at JNTU Hyderabad 2008.

Declaration:

I hereby declare that the information furnished above is true to the best of my knowledge and belief.

Station: Tirupathi

Date : 04-06-2020 Signature of Candidate

C.M.Sripriya