

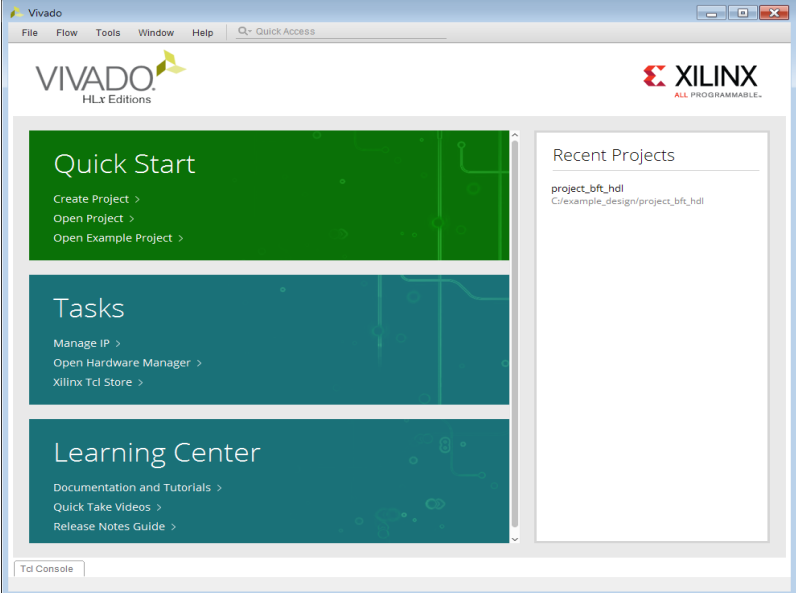
**SCHOOL OF ENGINEERING AND TECHNOLOGY
SRI PADMAVATHI MAHILA VISVAVIDYALYAM
(Women's University)**

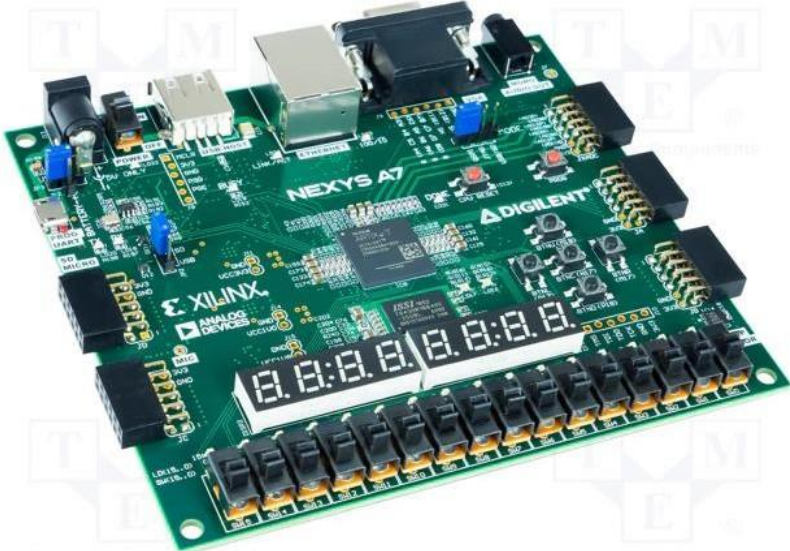
TIRUPATI

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

19MECP04: FPGA LAB

Front View of the Lab

S.No	Name of the Equipment / Software Tool	Specifications / Key equipment details / software tools specifications	Equipment Photo with Connections / Software Tool Home page
1	Xilinx Vivado 17 (Licensed for 25 Users)	<p>Software Tool Specifications:</p> <ul style="list-style-type: none"> • Tool Hardware Requirements: Processor: Intel Pentium 4, Intel Core Duo, or Xeon Processors; SSE2 minimum Memory/RAM: 2 GB or higher Supported Operating Systems: Windows 7 SP1 (64-bit) / Windows 8.1 (64-bit) / Windows 10 Pro (64-bit) • Lab System Requirements: Supported Operating Systems: Windows 10Pro 64-bit Computer Type: Desktop RAM: 8 GB Storage: 1TB. 	

2.	Artix-7 FPGA Board (5 Units)	Kit Specifications: <ul style="list-style-type: none">• 15,850 logic slices, each with four 6-input LUTs and 8 flip-flops• 4,860 Kbits of fast block RAM• Six clock management tiles, each with phase-locked loop (PLL)• 240 DSP slices• Internal clock speeds exceeding 450MHz• On-chip analog-to-digital converter (XADC)	
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