

**SCHOOL OF ENGINEERING AND TECHNOLOGY
SRI PADMAVATI MAHILA VISVAIDYALAYAM
SCHEME OF INSTRUCTION AND EVALUATION OF B.TECH (ECE)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC REGULATIONS, SCHEME and SYLLABII for R-2020**

COURSES IN MINOR PROGRAMME

THEORY													
S.No	Year / Semester	Course Code	Course Title	Hours per Week			Credits	Evaluation					Total Marks
								Internal (30 Marks)			External (70 Marks)		
				Assignment	Test			Duration	Max. Marks	Duration	Max. Marks		
												Max. Marks	
L	T	P											
1	II - II	20ECTM01	Analog and Digital Communication	3	1	0	4	5	2	25	3	70	100
2	III - I	20ECTM02	VLSI Signal Processing	3	1	0	4	5	2	25	3	70	100
3	III-II	20ECTM03	IC Applications	3	1	0	4	5	2	25	3	70	100
4		20ECTM04	Embedded Systems	3	1	0	4	5	2	25	3	70	100
5	IV-I	20ECTM05	Cellular Mobile Communication	3	1	0	4	5	2	25	3	70	100
Total				15	5	0	20						500