

CURRICULUMVITAE

Name & Designation : **Dr.V.Ramakrishna**
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Subject : Electronics and Communication
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I. Educational Details

QUALIFICATION	College/University	CLASS/ DIVISION
PhD, Specialization: VLSI	JNTU, Hyderabad	-
M.Tech (Microelectronics & VLSI Design)	RGPV, Bhopal	First Class
B.Tech (ECE)	JNTU, Hyderabad	First Class
Intermediate(MPC)	BIE, Hyderabad	First Class
SSC	BSE, Hyderabad	First Class

II. Employment Record (Starting from present position)

University/College	Designation	Period
SoET, SPMVV, Tirupati	Assistant Professor	Jun 2018 to Till Date
VCEW(Autonomous)	Assistant Professor	Jan 2016 to Mar 2018
SR Engineering College(Autonomous)	Assistant Professor	Jun 2009 to Nov 2015
KITS, Warangal	Lecturer	Jul 2007 to Apr 2009

III. Publications/ Presentations

International Journals : 11
International Conferences : 07
National Conferences : 06

IV. Patents Granted/Filed

Patents Granted : 01
Patents Filed : 01

V. Honors & Awards : 02 Awards

VI. Technical Skills

H/W Programming Languages VHDL and Verilog
Tools/Softwares Matlab, Cadence, Tanner Tools, Modelsim and Xilinx
Operating Systems Linux, Sun Solaris and Windows

VII. Projects

1. VLSI Implementation of MIMO Detection Algorithms
2. Designed main blocks which are used in image compression system using Verilog HDL. The design was made using Xilinx project navigator Tools for design entry, synthesized using XST (verilog), simulated using Modelsim and making the design to fit in the FPGA (Virtex-E xcv812e).
3. Designed low power switch capacitor based delta sigma modulator with minimal number of transistors. The tanner tool is used for the design.
4. Inter Integrated Circuit bus protocol Implementation: synthesize the design into the Xilinx XC 9500 -XC chip. HDL Designer was used for System modeling in VHDL.
5. Frequency Hopping for V/UHF Transceivers using Digital Signal processor, Organization: E.C.I.L, Hyd.

VIII. Training Programme / Seminars /Workshops Attended: 24

IX. Other Significant Contributions

1. Member - College Committee for NBA Accreditation Process in 2008-2009(KITSW), 2014-15 (SRECW) and 2017-2018(VCEW).
2. Member College Committee for NAAC Process in the year 2014-2015(SRECW) and 2017-2018(VCEW)
3. Member - SRECW College Committee for UGC Autonomous Process in the year 2013.
4. Setting various subject question Papers of Kakatiya University, KITS (Autonomous), CBIT (Autonomous) from 2013-2017.
5. Technical Committee member, "International Conference on Recent Advances in Communication, VLSI & Embedded Systems" organized by Department of ECE, SREC, during 19-20 December 2014.
6. Organizing committee member from ECE department ULTRIVA 13.
7. Sports Committee Member during the academic years 2010-2011, 2011-2012 and 2012-2013.
8. Organizing committee member in organizing 4th SR Champions Trophy for Staff Tournament-2012.
9. Organizing Committee Member, Short term summer course on "ARM Controllers and Applications".
10. Administrator, Curriculum Lead and Instructor for SR-CISCO Local Academy 2013-2014, 2014-2015

X. Other relevant information

Member of professional societies: 06

Projects Guided : M.Tech: 12, B.Tech: 24

Language Capabilities : Read, Write and Speak: English, Hindi and Telugu.

Countries Visited : Thailand