# RESUME


**Name** : Sistla V Sudheer Kumar
**Designation** : Assistant Professor
**Date of birth** : 06/05/1989
**Permanent Address** : 4-5-15, Chaduvulavari street, Woodpeta, Anakapalli- 531001, Visakhapatnam District, Andhra Pradesh.
**Subject** : Electronics and Communication Engineering
**ContactNumber** : 8106688666,9491333148
**Email ID** : sudheer.ssk39@gmail.com


**Summary**

- Possess **5** years of professional experience in teaching of Electronics and Communication subjects and possess one year of industrial experience.
- Published **6** papers in various international Journals and UGC Care Journals.
- Member in **5** professional societies.
- Currently acting as co-ordinator of **Industry Institute Intera(III)** committee of ECE Department in School of Engineering and Technology, Sri Padmavathi Mahila Visvavidyalayam, Tirupati.
- Attended number of Workshops, Conference and FDP's.

## I. EDUCATIONAL DETAILS

| Qualification | College/ University | Class | Year |
|---|---|---|---|
| Ph.D | KL University, Vijyawada | | Pursuing (DOJ: 15/07/2019) |
| M.tech (VLSI System Design) | Avanthi Institute of Engineering and Technology | First Class` | 2014 |
| B.Tech (ECE) | Avanthi Institute of Engineering and Technology | First Class | 2010 |
| Intermediate | Sri Kanya Junior College | First class | 2006 |
| SSC | Samyuktha KJM High School | First class | 2004 |

## II. EMPLOYMENT RECORD (Starting from present position)

### A. TEACHING EXPERIENCE -5 Years 9 Months

| University / College | Designation | Period |
|---|---|---|
| School of Engineering and Technology, Sri Padmavathi Mahila Visvavidyalyam, Tirupati. | Assistant Professor | 28th June 2018 to Till Date |
| Avanthi Institute of Engineering and Technology, Makavarapalem | Assistant Professor | 20th March 2014 to 20th May 2018 |

**B. INDUSTRIAL EXPERIENCE – 1Year**

| Name of the company | Designation | Period |
|---|---|---|
| Vishnu Chemicals, Parawada, Visakhapatnam District | Executive Engineer in Instrumentation Department | 1st December 2010 to 30th November 2011 |

## III. TECHNICAL SKILLS

Hardware Programming Languages: VHDL and Verilog.
Tools: Multisim, Xilinx, Tanner Tool, Mentor Graphics.

## IV. PUBLICATIONS/ PRESENTATIONS

**International Journals – Total : 06 (UGC Care Journal: 01)**

1. Sistla V Sudheer Kumar, B.N. Srinivasa Rao, E. Govinda. **Background Subtraction with Feature Extraction Based on FPGA**, International Journal of Engineering Trends and Technology (IJETT) – Volume 4 Issue 9- Sep 2013.
2. Badisa Sowmya Sri, S.V.Sudheer Kumar. **High Throughput Implementation Of 64 Bit Modified Wallance Mac Using Multioperand Adders**, (IJITR) International Journal Of Innovative Technology And Research, Volume No.4, Issue No.5, August – September 2016, 3861–3866.
3. Thota Sowjanya, S. V. Sudheer Kumar, E. Govinda. **Realization of AES Module for Low Latency Applications**, International Journal of scientific engineering and technology research, ISSN 2319- 8885, Volume 05,Issue.44, November-2016,Pages:9147-9151.
4. Korra Srinivas, S. V. Sudheer Kumar, **Functional Broad Side Tests for Embedded Logic Blocks**, International Journal of VLSI System design and communication systems. ISSN 2322-0929 Volume-06, Jan-Dec-2018, Pages:066-072
5. Satyaveni Pothala, S. V. Sudheer Kumar, **Non- Redundant Signed Digit Encoding by using Modified Booth Encoder**. International Journal of VLSI System Design and Communication Systems, ISSN 2322-0929, Volume-06, 2018.
6. Mainampati Sushma, S.V.Sudheer Kumar, **Execution of Median Filter Built on FPGA for Trimming Noise Meeting the Real Time Requirements**, International Journal of Recent Technology and Engineering (IJRTE), ISSN: 2277-3878, Volume- 8, Issue 04, November -2019, page no: 7855-7858.

**V. No. of workshops Co-Ordinator: 01**

| S.No. | Name of the workshop | Organized by | Date | Number of Days |
|---|---|---|---|---|
| 1. | eSim a First Course in the IoT Series for Teachers | School of Engineering and Technology, Sri Padmavathi Mahila Visvavidyalayam, Tirupati. In association with IIT Bombay. | 21/09/2019 | 01 |

**VI. Total No. of Workshops Attended: 09**

| S.No. | Name of the workshop | Organized by | Date | Number of Days |
|---|---|---|---|---|
| 1. | Effective and Efficient Online Teaching in the age of Corona | Indian Institute of Technology, Bombay | 16/05/2020 | 01 |
| 2. | eSim a First Course in the IoT Series for Teachers | Indian Institute of Technology, Bombay | 27/07/2019 | 01 |
| 3. | Research Methodology | KL Univeristy, Vijayawada | 16/07/2019 to 18/07/2019 | 03 |
| 4. | Assessment and Evaluation under Outcome Based Education | SPMVV, Tirupati. In association with NITTTR, Kolkata | 10/06/2019 to 14/06/2019 | 1 Week |
| 5. | Scilab | Sri Padmavathi Mahila Visvavidyalayam, Tirupati. In association with IIT Bombay | 04/05/2019 | 01 |
| 6. | NBA Accreditation | Sri Padmavathi Mahila Visvavidyalayam, Tirupati. In association with NITTTR, Kolkata | 22/04/2019 to 26/04/2019 | 1 Week |
| 7. | Big Data Analytics using Hadoop | Sri Padmavathi Mahila Visvavidyalayam | 1st, 2nd December 2018 | 2 |
| 8. | VLSITP-2016 | JNTU Kakinada | 21st July 2016 – 25th July 2016 | 5 |
| 9. | Analog And Digital Circuit Design Using Cadence | Gayathri Vidya Parishad College Of Engineering(Autonomous) | 9th-11th December 2015 | 3 |

**VII. No. of Conferences attended: 01**

| S.No. | Name of the Conference | Organized by | Date | Number of Days |
|---|---|---|---|---|
| 1. | Innovations in Applied Science and Engineering (NCIASE - 2019) | Dr B.R.Ambedkar NIT - Jalandhar | 27/04/2019 to 28/04/2019 | 02 |

**VIII. Total No. of Webnair's Attended: 10**

| S.No. | Name of the Webnair | Organized by | Date | Number of Days |
|---|---|---|---|---|
| 1. | Tools for Documentation: Conference, Journal and Thesis Writing | Vaagdevi college of Engineering(A) | 13/06/2020 | 1 |
| 2. | Patent Searching, Drafting and Filing | RVS College of Engineering and Technology, kannampalayam Sulur, coimbatore | 17/05/2020 | 1 |
| 3. | JGate @ E-Shodhsindhu | Informatics Publishing Limited, Bangalore | 15/06/2020 | 1 |
| 4. | 3D Cable Modelling in Comsol Multiphysics | IEEE | 24/06/2020 | 1 |
| 5. | Advanced Nanomaterials and NanoDevices for Electronic Applications | KL University | 22/06/2020 | 1 |
| 6. | Circuit simulation and Compact Modelling beyond Device Physics | Velalar College of Engineering and Technology, Erode | 24/07/2020 | 1 |
| 7. | Next Generation Smart sensors | BITS Pilani, Hyderabad | 31/07/2020 | 1 |
| 8. | Design approaches for Frontend VLSI Design | Vaagdevi college of Engineering(A) | 23/05/2020 | 1 |
| 9. | VLSI | Andhra Loyola Institute of Engineering and Technology | 10/06/2020 to 12/06/2020 | 3 |
| 10. | MEMS System Design | Vaagdevi college of Engineering(A) | 27/05/2020 | 1 |

**IX. Total No. of FDP's Attended: 10**

| S.No. | Name of the FDP | Organized by | Date | Number of Days |
|---|---|---|---|---|
| 1. | Basics of Software Defined Radios and Practical applications | NPTEL, IIT Roorkee | January to February 2019 | 4 Week |
| 2. | FDP101x- Foundation Program in ICT for Education FDP201x- Pedagogy for Online and Blended Teaching-Learning Process | IIT-Bombay | 30th October 2018 – 7th January 2019 | 2 months, 8 days. |
| 3. | Low Power VLSI Design | NITTTR, Chandigarh | 20th April 2020 – 24th April 2020 | 1 week |
| 4. | Internet of Things(IoT) | ATAL Academy, IIITNagpur | 25th April 2020 – 29th April 2020 | 1 Week |
| 5. | NanoMaterials & Devices | NITTTR, Chandigarh | 27th April 2020 – 01st May 2020 | 1 week |
| 6. | Recent Trends in Advanced Communications | Lakireddy Bali Reddy College of Engineering(A), Mylavaram | 04th May 2020- 09th May 2020 | 1 week |
| 7. | Research, Funding Projects & IPR | K.C. College of Engineering & Management studies & Research, Thane | 7th May 2020 – 10th May 2020 | 4 days |
| 8. | Modern Research Trends in Communication, Signal Processing and VLSI/MEMS. | Andhra Loyola Institute of Engineering and Technology, Vijayawada | 19th May 2020 – 23rd May 2020 | 5 Days |
| 9. | Design approaches for Frontend VLSI Design | Vaagdevi college of engineering(A), Warangal | 23rd May 2020 | 1 day |
| 10. | MEMS System Design | Vaagdevi college of engineering(A), Warangal | 27th May 2020 | 1 day |

**X. Membership of professional societies:**

1. Member, IEEE, Membership No. 96885964
2. Member, International Association of Engineers (IAENG), Membership No. 235207.
3. Member, The European Association for Signal Processing (EURASIP), Membership No. 8949.
4. Member, International Association of Online Engineering (IAOE), Membership No. 10388.
5. Associate Member of Universal Association of Computer and Electronics Engineers (UACEE), Membership No. AM101000583573.

## XI. Other relevant information

Projects Guided: M.Tech: 06, B.Tech: 08

Language Capabilities - Read, Write and Speak: English, Hindi and Telugu.

## XI. List of Subjects Taught:

**UG**: Electronic Devices and Circuits, Switching theory and logic design, Digital system design and Digital IC applications, VLSI Design, Electronic Circuit Analysis, Electronic Measurements and Instrumentation.

**PG**: CMOS VLSI Design, CPLD and FPGA Architectures and Applications, Digital system design, RFID Systems.

## XII. Area of Research:

- NanoMaterials and Nano Devices.

**(S.V.SUDHEER KUMAR)**