

Profile

ch.kavya

Assistant professor
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Academic Qualifications

Qualification	Name of the Institution	Board / University	Year of Study	Percentage(%) of Marks
M.Tech (VLSI)	Sreevidyanikethan engineering college	Sreevidyanikethan engineering college, rangampeta	20102012	85.75
B. Tech in electronic & communication Engineering (E.C.E)	Sri Padmavathi mahila visvavidyalayam, Tirupati	Women's university, Tirupati.	20062010	75.05
Intermediate (M.P.C)	Chaitanya College, Tirupati.	Board of Intermediate Education, Andhra Pradesh	20042006	93.33
School (C.B.S.E)	Kendriya vidyalayam Tirupati.	Central Board of Secondary Education	20032004	68.8

Total Experience:5 YEARS

Organization	Position	From	To	No. of Years
SPMVV	Assistant professor	2015	Till date	5years
SPMVV	Guest faculty	2013	2014	1year

Areas of Research Interest:

- VLSI design
- Digital Electronics

List of Publications in International Journals:

- 1.P. ANNAPURNA, CH. KAVYA, M. MUNISANKAR“**Accomplishment of HARQ-IR for energy scavenging wireless sensor networks**”, ICIRCA2018 ,IEEE PAPER, ISBN: 978-5386-2456-2.
2. CH.Kavya,(Nov-2019) “**R Peak and ARRHYTHMIA Detection in ECG based on NLM and wavelet thresholding**” , IJAEMA, An UGC -CARE Approved group- A Journal , ISSN NO: 0886-9367.
- 3R.priyadarsini, C.H kavya (May -2017) Fixed-Outline 3-D IC Floor planning With TSV Co-Placement in IRJET (e-ISSN: 2395 -0056).
- 4.CH. KAVYA, B. ANUSHA, T. LAKSHMIPRASANNA (30th May 2017)“**Image Steganography in DWT domain.**” INIJRCCE(ISSN: 2320-9798).
- 5.Ch.kavya,R.priyadarshini,B.madhavi(May 2017) “**Calories and nutrition measurement from the image of food.**” In IJARIIT.(ISSN:2454-132X).
- 6.Kavya.ch,Priyadarsini.R, Madhavi.B, (March 2017)“**Modern Garbage Bin Proctor using GSM.**” In ICIAEIT-2017(ISSN:2454-356X).
- 7.R.priyadarsini , C.H kavya(2017) **SIGNAL PROCESSINGSEGMENTED SERIAL PARALLELMULTIPLIER USING VLSI** in IJIR(ISSN:2454-1362).
- 8.CH.Kavya, K.Neelima, A.Ramadevi (MARCH 2017) “**DETECTION AND TRACKING OF FACIALFEATURES IN VIDEO SEQUENCES .**” IN International Journal of InnovativeResearch in Computer and Communication Engineering(IJERECE) , (ISSN:2394-6849).
9. B. Sandhya,V. Sri Lakshmi Priyanka,CH. Kavya “**Detection and Tracking of Facial Features in Video Sequences**” (March 2017) in IJERECE(ISSN:2320-9798).
- 10.R. Priyadarsini, C.H Kavya,A.Lasmika (March 2017) “**An Experimental Power Lines Model For Digital ASICS Based On Transmission Lines.**” In ICIAEIT-2017 ,(ISSN:2454-356X).
- 11.E. TEJASWI, CH. KAVYA (November-2016) “**Sparse representation based greedy search for face sketch synthesis.**” IN IJSETR, (ISSN:2319-8885) .
12. Naga UdayiniNyapathi, BhargaviPendlimarri, KarishmaSk , Kavya Ch (May-2016) “**Smart Medicine Box using ARM 7 Microcontroller.**” In IRJET ,(e-ISSN:2395-0056).
- 13.Kavya Cheraukula, Chowdam Venkata Sudhakar (June 2012)“**Design and implementation of memory for joint improvement of error tolerance and access efficiency.**” in IJEAT , (ISSN:2249-8958).

List of Publications in International Conferences:

1. Annapurna durga, ch.kavya, munishenkar (July 12th 2018) “**accomplishment of Harq-IR for energy scavenging wireless sensor networks**”. In **IEEE conference**, i.e. international conference on inventive research in computing applications (**ICIRCA 2018**)

2. B. Sandhya, V. Sri Lakshmi Priyanka, CH. Kavya “**Detection and Tracking of Facial Features in Video Sequences**.” In international conference on emerging trends in engineering, sciences and technologies (**ICETEST-2017**)

3. R. priyadarshini, ch.kavya, A. Lasmika (March 2017) “**An Experimental Power Lines Model For Digital ASICs Based On Transmission Lines**.” In international conference on innovative applications in engineering and information technology (**ICIAEIT-2017**)

4. kavya.ch, priyadarshini.R, madhavi.b “**Modern Garbage bin proctor using GSM**.” In international conference on innovative applications in engineering and information technology (**ICIAEIT-2017**)

List of Publications in National Conferences:

1. R. priyadarshini, ch.kavya (March 31st) “**Efficient implementation of digital receiver using FPGA**.” National conference on signal and information processing (**NCSIP-2K17**)

List of Events (Conferences/Seminars/Workshops/FDPs/Training Programs/...) attended:

S.No.	Name of the Event	Organized by	Date	Number of Days
1.	NPTEL -AICTE FDP	AICTE	FEB, 2019	1 WEEK
2.	TRAINING PROGRAMME THROUGH ICT MODE	NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING RESEARCH	10-06-2019 TO 14-06-2019	1 WEEK
3.	AICTE approved faculty development programme (FDP101X)	IIT BOMBAY	13-09-2018 TO 18-09-2018	1 WEEK
4.	Two-day workshop on BIG DATA ANALYTICS USING HADOOP	LeadingIndia.ai	1 st & 2 nd dec, 2018	2 days
5.	Three days workshop on signal & image processing	S.V.U under CoE (TEQIP-II-1.2.1)	2 nd -4 th march, 2017	3 days
6.	7 days entrepreneurship	S.V.U	Oct 2 nd -8 th	7 days

	development training for faculty	Entrepreneurship development cell	2016	
7.	Two days workshop on patent information cell	A.P state council of science and technology	30 th & 31 st march ,2016	2days
8.	A two-day faculty development programme on MATLAB applications to engineering problems	Annamacharya institute of technology and sciences(Dept. of E.C.E)	Nov 27-28, 2015	2 days

NPTEL COURSES:

COMPLETED THREE NPTEL COURSES:

1. NPTEL ONLINE COURSE ON **CMOS DIGITAL VLSI DESIGN** , 8 WEEKS COURSE (Feb-Apr 2019).
2. NPTEL ONLINE COURSE ON **DIGITAL CIRCUITS**, 12 WEEKS COURSE (JUL-OCT 2019).
3. NPTEL ONLINE COURSE ON **MICROELECTRONICS**, 12 WEEKS COURSE (JUL-OCT 2019).