

CURRICULUM VITAE

R.Sankeerthana
Tirupati

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CAREER OBJECTIVE:

Seeking for a position in an organization where I can use my abilities and skills in providing the students with the best techniques, in order to get best out of them.

ACADEMIC PROFILE:

Qualification	Institute	Board/ University	Year of Passing	Aggregate
M.E (VLSI Design)	K.C.G college of technology	ANNA University	2011	8.6 (CGPA) RANK HOLDER
B.TECH [E.C.E]	SVP College of Engineering & Technology	J.N.T.U	2009	68%
IPE	CAN Junior College	Board of Intermediate Education	2005	74%
CBSE	Kendriya Vidyala No.1	Central Board of Secondary Education	2003	66%

WORK PROFILE:

- Working as Assistant Professor in **SRI PADMAVATHI MAHILA UNIVERSITY**, Tirupati, from June 2014 to till date.
- Worked as a **CADD Designer** in Cambridge Technologies, Bangalore, from March 2012 to Sep 2012.

SUBJECTS HANDLED:

- Embedded systems
- Satellite communications
- Electronic materials and devices
- Telecommunication switching systems
- Basic Electronics & Engineering

- VHDL
- VLSI Design
- Digital logic design
- Microprocessors and Interfacing

PUBLICATIONS/CONFERENCES/WORKSHOPS:

- I. R.Sankeerthana, paper entitled “**COMPARISON OF AUTOMATIC IMAGE CONTRAST ENHANCEMENT USING GAUSSIAN MIXTURE MODEL AND GLOBAL HISTOGRAM EQUALIZATION**”, **International Journal of Computer Science Trends and Technology**, volume 2, issue 5-2014.
- II. Presented National Conference entitled “**Pipelined implementation of Full search Motion Estimation for video compression using variable block size**” in Chennai.
- III. Presented National conference on “**NORMALIZATION ON FLOATING POINT MULTIPLICATION USING VERILOG HDL**”, IITE 2K13 April 2013.
- IV. Presented National conference on “**AN EFFICIENT CARRY SELECT ADDER WITH REDUCED AREA APPLICATION**”, VLSI'13 March 2013.
- V. Presented National conference on “**A HYBRID MASKING SCHEME USING CONTOURLET TRANSFORM AND SINGULAR VALUE DECOMPOSITION**”, **ELIXIR**.
- VI. “**PROGRESSIVE IMAGE COMPRESSION USING CONTOURLETS TRANSFORM**”. National conference on Convergence of signal processing communication and VLSI design.
- VII. Participated in a two- week ISTE WORKSHOP on “**INTRODUCTION TO RESEARCH METHODOLOGIES**”, **JULY '2013**.
- VIII. Participated in 10-day workshop on “**TEACHING METHODOLOGIES**”, **BY MISSION 10X**, Feb 2013.
- IX. Participated in a 5 DAY IUCEE WORKSHOP on “**ADVANCED TRENDS IN VLSI**”, **JULY 2014**.
- X. Participated in National level **Faculty Development Programme** on Embedded Systems by **TEQIP** in Dec 2014.
- XI. Attended various FDP's and webinars in pandemic period.

TECHNICAL SKILLS :

Languages : C, VHDL, Verilog.
 Tools : Cadence, Xilinx, Modelsim, Matlab.

PROJECT PROFILE:

B.Tech :

Project Title : Real-time Implementation of Library Management using RFID and GSM
Platform : EMBEDDED SYSTEMS
Software : EMBEDDED - 'C' & KEIL

Project Description :

The goal of the project is to automate library management. The system will automate librarian's tasks, improve efficiency in book keeping, issue & return process and add convenience to the library members. Books were tagged with an RFID tag and a RFID reader station was used to identify issuance and return process. The GSM modem was used for notification purposes.

M.E :

Project Title : Pipelined implementation of Full search Motion Estimation for video compression using variable block size.
Platform : MODELSIM
Software : Verilog.

Project Description :

Hexagon based search pattern in block motion estimation, which demonstrates significant speedup gain over the other search algorithms. The hexagon based search scheme finds the motion vector in motion field with fewer search points than the DS algorithm.

PERSONAL PROFILE:

Name : R. Sankeerthana
Nationality : Indian
Gender : Female
Marital Status : Married
Date Of Birth : 10th August, 1988
Hobbies : Singing
Languages Known : English, Hindi, Telugu, Kannada, Tamil

DECLARATION:

I hereby declare that the information furnished above is true to best of my knowledge.

Place:

Date:

(R.Sankeerthana)