

Profile



PARDHAVI SAI SREE T

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Academic Consultant,
Department of Electrical and Electronics Engineering (EEE)
School of Engineering & Technology,
Sri Padmavati Mahila Visvavidyalayam,
Tirupati – 517502,
Andhra Pradesh, India.

Academic Credentials

Degree	University	Year	Specialization
M.Tech	JNTUA	2014	Electrical Power Systems
B.Tech	JNTUA	2011	Electrical and Electronics Engineering

Teaching Experience

Organization	Designation	Period
Sri Padmavati Mahila Visvavidyalayam, Tirupati	Academic Consultant	02-08-2022 to Till Date
Sri Venkateshwara University College of engineering, SVU, Tirupati	Academic Consultant	08-08-2019 to 30-05-2020
Sri Padmavati Mahila Visvavidyalayam, Tirupati	Assistant Professor(Junior)	19-06-2017 to 30-04-2018
Sri Padmavati Mahila Visvavidyalayam, Tirupati	Academic Consultant	25-06-2016 to 30-04-2017
Seshachala Polytechnic College	Assistant Professor	22-01-2015 to 26-05-2016

Publications and Presentations

Publications in referred journals	05
Publications in International Conference	-

Selected Key Publications

1. Lakshmi Sindura M , Pardhavi Sai Sree T “**A New Modified Three Phase Multilevel Inverter with Reduced Number of Switches by using Different PWM Techniques**” paper in the “International Journal of Scientific Engineering and Technology Research(IJSETR)”, ISSN: 2319-8885, Volume 05, Issue 48, December-2016, PP:9865-9870.
2. Y.Shiva Jyothi, Pardhavi Sai Sree T “**Brushless DC Motor Drive Based on Fuzzy Logic Controller for Solar PV Fed Water Pumping System Using Zeta Converter**” paper in the “International Journal of Innovative Research in Science, Engineering and Technology (IJIRSET)”, ISSN: 2319-8753, Volume 6, Issue 9, September 2017.
3. Nirmala Kesavaram, Pardhavi Sai Sree T “**Enhancement of Optimal Placement of Shunt Connected FACTS Device In A Series Compensated Long Transmission Line**” paper in the “International Journal of Latest Engineering and Management Research (IJLEMR)”, ISSN: 2455-4847, REETA-2K16 , PP. 417-424.
4. Nirmala Kesavaram , Pardhavi Sai Sree T “**Development of A Fuzzy Logic Controller Using PV Maximum Power Point Tracking**” paper in the “International Journal of Latest Engineering and Management Research (IJLEMR)”, ISSN: 2455-4847, REETA-2K16 , PP. 271-280.
5. R.Pavan Kumar Naidu, Pardhavi Sai Sree T “**Renewable and Dispatchable DG Allocation for Maximum Benefits**” paper in the “International Journal of Latest Engineering and Management Research (IJLEMR)”, ISSN: 2455-4847, REETA-2K16 , PP. 281-292

B.Tech and M.Tech Project Works Supervised

M.Tech Dissertations guided	:	02
B.Tech Project Works guided	:	02+01(pursuing)

Administrative Positions

- Worked as Examination Coordinator for EEE Department from 2016 to 2018, SOET, SPMVV
- Worked as Cultural Coordinator for EEE Department from 2016 to 2018, SOET, SPMVV
- Worked as Member of Admission Committee from 2016 to 2018, SOET, SPMVV

Courses Taught	
B.Tech	M.Tech
Control Systems	Power Quality
HVDC	Energy Auditing and Management System
Power Systems	
Basic Electrical Engineering	
Power Quality	
Electrical Machines II	
Microprocessor and Microcontrollers	

I hereby declare that the above information is true to the best of my knowledge.

Date:

Place: Tirupati

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